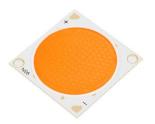


Enable High Flux and Cost Efficient System

Acrich Chip on Board – MJT COB series SAW*33EAA (SAW833EAA, SAW933EAA)













Product Brief

Description

- The MJT series are LED arrays which provide High Flux and High Efficacy.
- It is especially designed for easy assembly of Lighting fixtures by eliminating reflow soldering process.
- It's thermal management is excellent than other power LED solutions with wider Metal area.
- The MJT series are ideal light sources for General Lighting applications including Replacement Lamps, Industrial & Commercial Lightings and other high Lumen required applications.

Features and Benefits

- Efficacy up to 160lm/W @5000K
- Size 38mm * 38mm
- MacAdam 2-step & 3-step binning
- Uniformed Shadow
- · Excellent Thermal management
- RoHS compliant
- UL recognized component(E359235)

Key Applications

- Out door area Bay lighting, Street lighting, Tunnel lighting
- Architectural Spot lighting
- Industrial

Table 1. Product Selection Table

Part Number		CRI		
rait Nullibel	Color	Min.	Max.	Min
	Cool White	4700K	7000K	80
SAW833EAA	Neutral White	3700K	4200K	80
	Warm White	2600K	3700K	80
SAW933EAA	Neutral White	3700K	4200K	90
	Warm White	2600K	3700K	90



Table of Contents

Inde	ex ·	
•	Product Brief	1
•	Table of Contents	2
•	Performance Characteristics	3
•	Characteristics Graph	5
•	Color Bin Structure	16
•	Mechanical Dimensions	18
•	Packaging Specification	19
•	Product Nomenclature (Labeling Information)	21
•	Handling of Silicone Resin for LEDs	22
•	Precaution for Use	23
•	Company Information	26

Performance Characteristics

Table.2 Electro Optical Characteristics, T_i=85°C

Part Number	сст (к) ^[1]	Typical Luminous Flux ^[2] Φ _V ^[3] (lm)	Typical Forward Voltage (V _F) ^[4]	CRI ^[5] ,	Viewing Angle (degrees) 20 ½
	Тур.	1.5A	1.5A	Min.	Тур.
	6500	18,625	80.2	80	118
	5700	18,808	80.2	80	118
	5000	19,027	80.2	80	118
SAW833EAA	4000	18,899	80.2	80	118
	3500	18,534	80.2	80	118
	3000	18,260	80.2	80	118
	2700	17,621	80.2	80	118
	4000	15,886	80.2	90	118
CAM022EAA	3500	15,521	80.2	90	118
SAW933EAA	3000	15,339	80.2	90	118
	2700	14,718	80.2	90	118

Notes:

- (1) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram. Color coordinate : ± 0.005 , CCT $\pm 5\%$ tolerance.
- (2) Seoul Semiconductor maintains a tolerance of $\pm 7\%$ on flux and power measurements.
- (3) Φ_{V} is the total luminous flux output as measured with an integrating sphere.
- (4) Tolerance is $\pm 3\%$ on forward voltage measurements.
- (5) Tolerance is ± 2 on CRI measurements.

^{*} No values are provided by real measurement. Only for reference purpose.

Performance Characteristics

Table.3 Absolute Maximum Ratings

Dozomotov	Sumbol		Unit		
Parameter	Symbol	Min.	Тур.	Max.	Unit
Forward Current	I _F	-	1.5	3.0	Α
Power Dissipation	P_d	-	120	257.3	W
Junction Temperature	T_j	-	-	140	°C
Operating Temperature	T_{opr}	- 40	-	100	°C
Surface Temperature	T _S	- 40	=	105	°C
Storage Temperature	T_{stg}	- 40	-	105	°C
Thermal resistance (J to S) [1]	$R\theta_{J-S}$	-	0.13	-	K/W
ESD Sensitivity(HBM)		Class	3A JESD22-A	114-E	

Notes:

- (1) Thermal resistance : $R\theta_{J-S}$ (Junction / solder) At thermal resistance, J to S means junction to COB's substrate bottom.
- (2) LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- (3) Thermal resistance can be increased substantially depending on the heat sink design/operating condition, and the maximum possible driving current will decrease accordingly.
- (4) All measurements were made under the standardized environment of Seoul Semiconductor.

Fig 1. Color Spectrum, CRI80

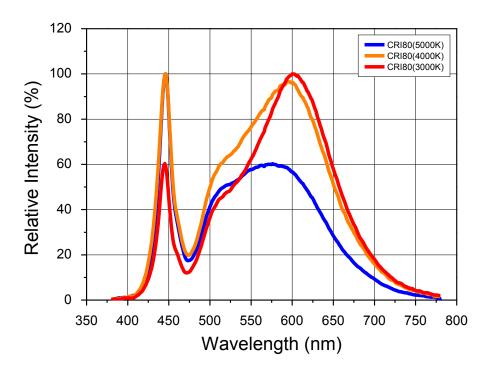


Fig 2. Color Spectrum, CRI90

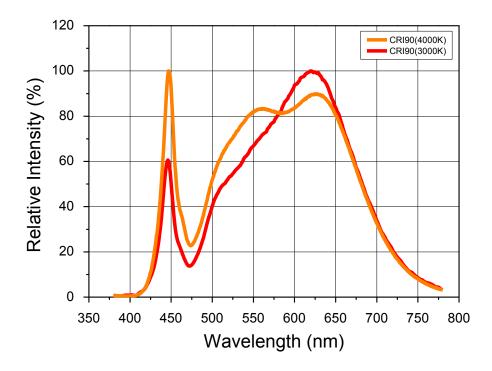


Fig 3. Radiant Pattern

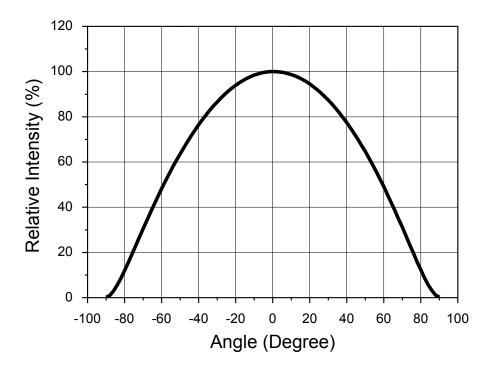


Fig 4. Forward Voltage vs. Forward Current, T_i=85°C

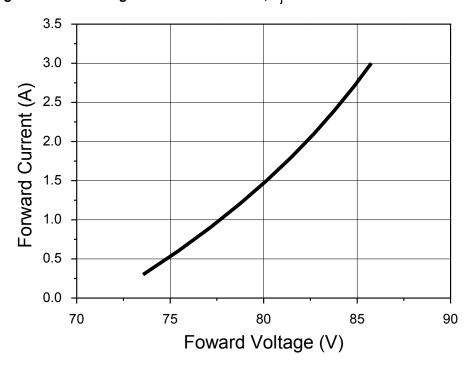


Fig 5. Forward Current vs. Relative Luminous Flux, T_i=85°C

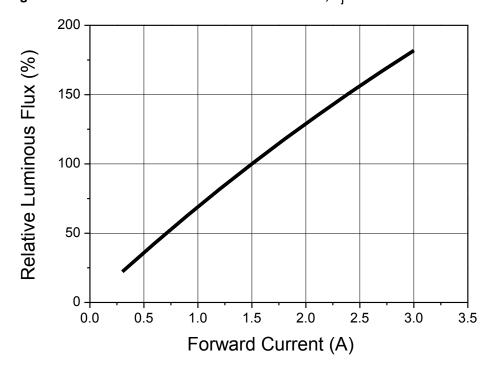


Fig 6. Junction Temperature vs. Relative Luminous Flux, I_F=1.5A

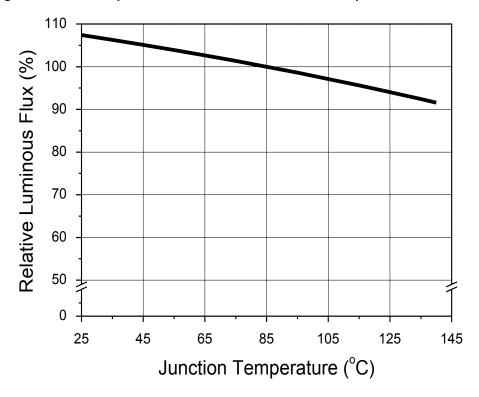


Fig 7. Junction Temperature vs. Forward Voltage, I_F=1.5A

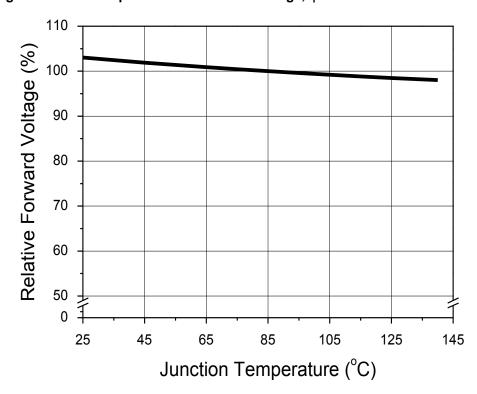


Fig 8. Junction Temperature vs. CIE x,y Shift, I_F=1.5A (CRI80, 5000K)

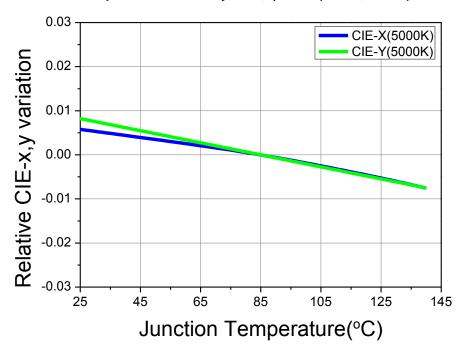


Fig 9. Junction Temperature vs. CIE x,y Shift, I_F=1.5A (CRI80, 4000K)

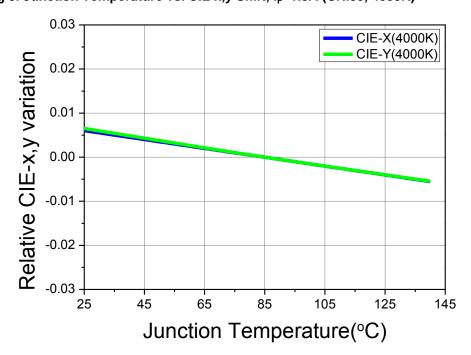


Fig 10. Junction Temperature vs. CIE x,y Shift, I_F=1.5A (CRI80, 3000K)

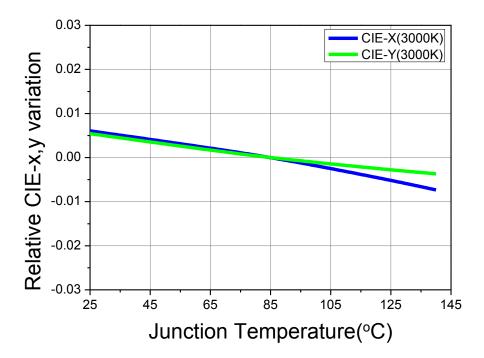


Fig 11. Junction Temperature vs. CIE x,y Shift, I_E=1.5A (CRI90, 4000K)

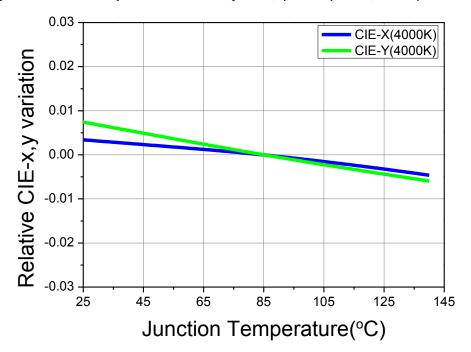


Fig 12. Junction Temperature vs. CIE x,y Shift, I_F=1.5A (CRI90, 3000K)

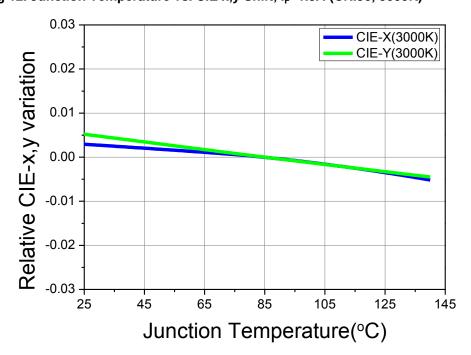


Fig 13. Forward current vs. CIE x,y Shift, T_i=85°C (CRI80, 5000K)

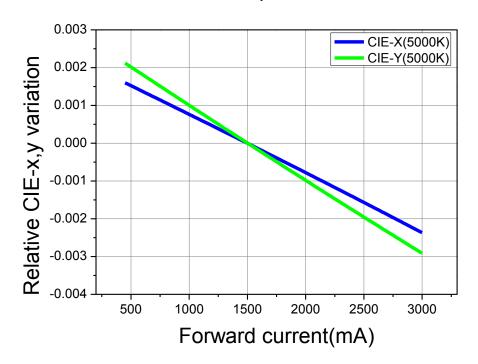


Fig 14. Forward current vs. CIE x,y Shift, T_i=85°C (CRI80, 4000K)

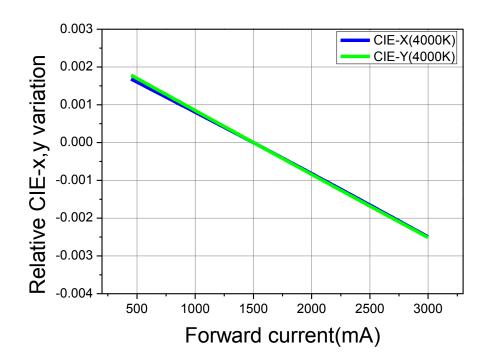


Fig 15. Forward current vs. CIE x,y Shift, T_i=85°C (CRI80, 3000K)

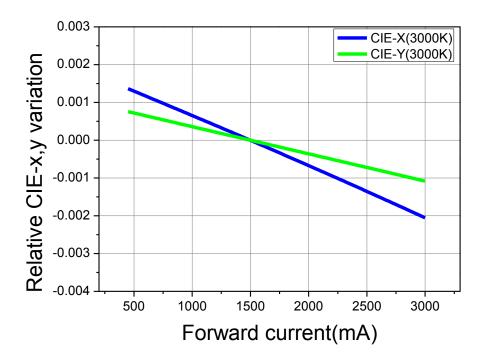


Fig 16. Forward current vs. CIE x,y Shift, T_i=85°C (CRI90, 4000K)

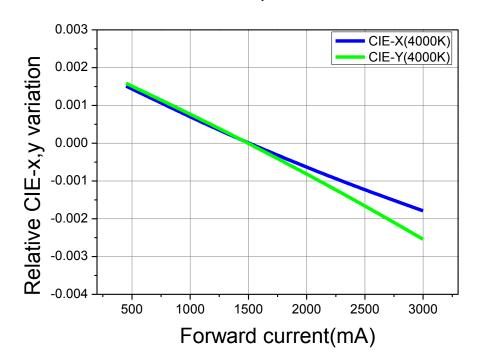


Fig 17. Forward current vs. CIE x,y Shift, T_i=85°C (CRI90, 3000K)

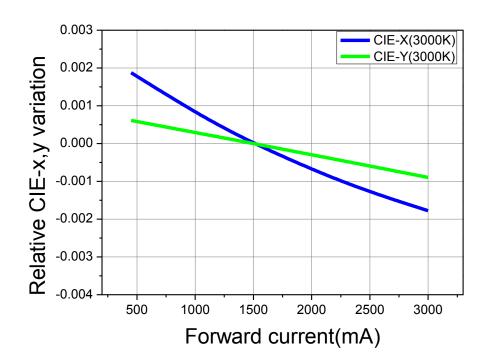
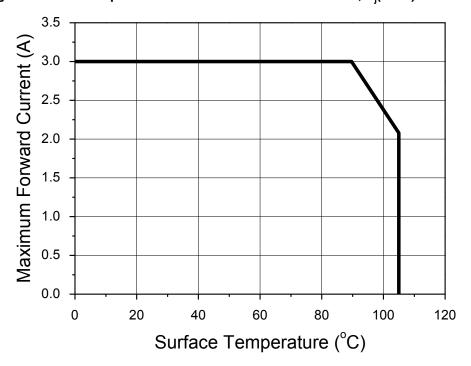


Fig 18. Surface Temperature vs. Maximum Forward Current, T_i(max.)=140°C



Color Bin Structure

Table 4. Bin Code description, T_j =85°C, I_F =1.5A

Luminous Flux Part (lm)			Color Chromaticity		Typical Forward Voltage (V)		CRI			
Number	Bin Code	Min.	Тур.	Bin Code	Typ. CCT	Bin Code	Min.	Max.	Bin Code	Min
	M0	17,135	18,625	AE3	6500K	0	76.1	84.2	8	80
	M0	17,303	18,808	BE3	5700K	0	76.1	84.2	8	80
	MO	17,505	19,027	CE3	5000K	0	76.1	84.2	8	80
SAW833EAA	MO	17,387	18,899	EE3	4000K	0	76.1	84.2	8	80
	MO	17,051	18,534	FE3	3500K	0	76.1	84.2	8	80
	MO	16,799	18,260	GE3	3000K	0	76.1	84.2	8	80
	MO	16,211	17,621	HE3	2700K	0	76.1	84.2	8	80
	MO	14,615	15,886	EE2 EE3	4000K	0	76.1	84.2	9	90
CAM/0225AA	MO	14,279	15,521	FE2 FE3	3500K	0	76.1	84.2	9	90
SAW933EAA	MO	14,111	15,339	GE2 GE3	3000K	0	76.1	84.2	9	90
	MO	13,540	14,718	HE2 HE3	2700K	0	76.1	84.2	9	90

Color Bin Structure

CIE Chromaticity Diagram, T_i=85℃

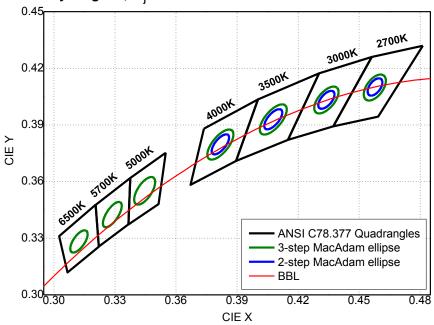


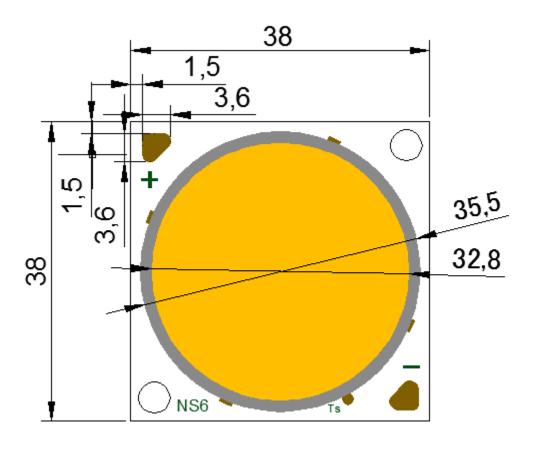
Table 5. 2-step/3-step MacAdam ellipse color bin definitions

Color Region	or Region CCT Center Point Major Axis		Minor Axis	Rotation Angle (θ)			
	(K) CIE x CIE y	(b)					
	6500	0.3123	0.3283	0.00669	0.00285	58.38	
	5700	0.3287	0.3425	0.00760	0.00296	59.46	
3-step	5000	0.3446	0.3551	0.00822	0.00354	59.62	
MacAdam	4000	0.3818	0.3797	0.00939	0.00402	54.00	
ellipse	3500	0.4078	0.393	0.00951	0.00417	52.97	
	3000	0.4339	0.4033	0.00834	0.00408	53.17	
	2700	0.4578	0.4101	0.00774	0.00411	57.28	
Color Region	ССТ	Cente	Center Point		Minor Axis	Rotation Angle	
	(K)	CIE x	CIE y	(a)	(b)	(θ)	
	4000	0.3818	0.3797	0.00626	0.00268	54.00	
2-step MacAdam	3500	0.4078	0.393	0.00634	0.00278	52.97	
ellipse	3000	0.4339	0.4033	0.00556	0.00272	53.17	
	2700	0.4578	0.4101	0.00516	0.00274	57.28	

Notes:

- (1) The chromaticity center refers to ANSI C78.377:2015.
- (2) (a), (b), and (θ) indicate the major axis length, the minor axis length, and the rotation angle from the X axis of the ellipse bin, respectively.
- (3) It's possible to deliver 2-step bin up to 4000K.

Mechanical Dimensions

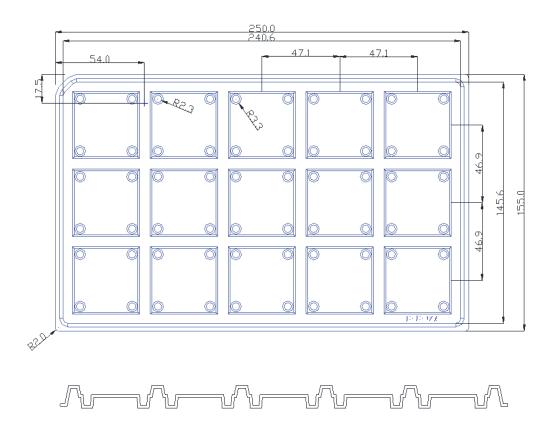




Notes:

- 1. All dimensions are in millimeters.
- 2. Scale: none
- 3. Undefined tolerance is ± 0.2 mm

Packaging Specification



Notes:

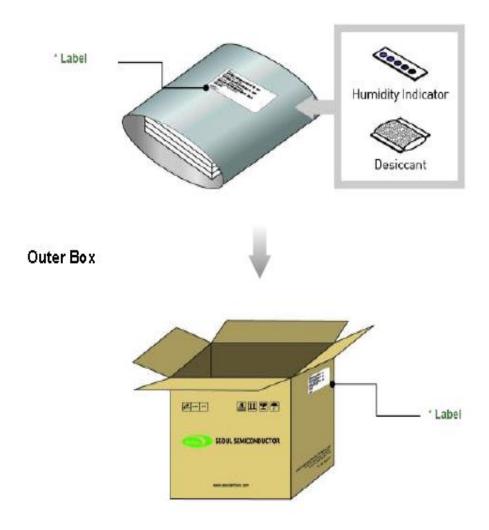
(1) Quantity : 15pcs/Tray

(2) All dimensions are in millimeters (tolerance : $\pm 0.3)\,$

(3) Scale none

Packaging Specification

Aluminum Bag



Notes:

(1) Heat Sealed after packing (Use Zipper Bag)

(2) Quantity: min 6Tray(90pcs) ~ max 10Tray(150pcs) / Bag

(3) Smallest packing quantity: 1Bags(90pcs) / large box

Product Nomenclature

 $RANK: Z_1Z_2Z_2Z_2Z_3Z_4$

QUANTITY: 90~150

LOT NUMBER : $Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$

SSC PART NUMBER : $X_1X_2X_3X_4X_5X_6X_7X_8X_9$



Table 5. Part Numbering System: X₁X₂X₃X₄X₅X₆X₇X₈X₉

Part Number Code	Description	Part Number	Value	
X ₁	Company	S		
X ₂	Package series	Α		
V V	Calan Cassification	W8	CRI 80	
X ₃ X ₄	Color Specification	W9	CRI 90	
X ₅ X ₆	LES size	33		
X ₇	Chip Array	E	Series	
X ₈	Chip Array	Α	Parallel	
X ₉	Revision number	Α		

Table 6. Lot Numbering System : $Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$

Lot Number Code	Description
$Y_1Y_2Y_3Y_4Y_5$	Date of box packing
$Y_6Y_7Y_8Y_9Y_{10}$	Date of label order
Y ₁₁ Y ₁₂ Y ₁₃ Y ₁₄ Y ₁₅ Y ₁₆ Y ₁₇	Item code

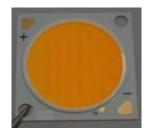
Handling of Silicone Resin for LEDs

(1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.





- (3) Silicone differs from materials conventionally used for the manufacturing of LEDs.
 These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust. As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of wire.
- (4) Seoul Semiconductor suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (5) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this product with acid or sulfur material in sealed space.
- (6) Avoid leaving fingerprints on silicone resin parts.



Precaution for Use

(1) Storage

To avoid the moisture penetration, we recommend storing LEDs in a dry box with a desiccant. The recommended storage temperature range is 5°C to 30°C and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use SMT techniques properly when you solder the LED as separation of the lens may affect the light output efficiency.

Pay attention to the following:

- a. Recommend conditions after opening the package
 - Sealing / Temperature : 5 ~ 40°C Humidity : less than RH30%
- b. If the package has been opened more than 4 week(MSL_2a) or the color of the desiccant changes, components should be dried for 10-12hr at $60\pm5^{\circ}$ C
- (3) Radioactive exposure is not considered for the products listed here in.
- (4) Gallium arsenide is used in some of the products listed in this publication. These products are dangerous if they are burned or shredded in the process of disposal. It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.
- (5) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.
- (6) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.
- (7) LEDs must be stored in a clean environment. We recommend LEDs store in nitrogen-filled container.
- (8) The appearance and specifications of the product may be modified for improvement without notice.
- (9 Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.
- (10) Attaching LEDs, do not use adhesive that outgas organic vapor.
- (11) The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.
- (12) Please do not touch any of the circuit board, components or terminals with bare hands or metal while circuit is electrically active.



Precaution for Use

(13) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.

(14) LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.

a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)



Precaution for Use

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
 (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires.
- This damage usually appears due to the thermal stress produced during the EOS event.
- c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:
 - A surge protection circuit
 - An appropriately rated over voltage protection device
 - A current limiting device



Company Information

Published by

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Company Information

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

Legal Disclaimer

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